





<b>Substitute for form 1449</b> <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	09/122,349
				Filing Date	July 24, 1998
				First Named Inventor	Lance Hacking
				Art Unit	2186
				Examiner Name	Tran, D.
				Attorney Docket Number	42390P5965
Sheet	2	of	2		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DT		Case, Brian, "Intel Reveals Next-Generation 960 H-Series", 1994 <u>MicroDesign Resources</u> , Vol. 8, No. 13, 10/03/1994, pp. 1-5	
DT		Baron, Max et al., "32-bit CMOS CPU chip acts like a mainframe", <u>Electronic Design</u> , April 16, 1987, pp. 95-100.	

Examiner Signature	<i>Newman</i>	Date Considered	10/3/05
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>1</sup>Applicant's unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/088 (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.  
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